



Company	PYXALIS
Place	MOIRANS (38) - Centr'Alp – France
Job title	Design Leader

**CONTEXT**

Pyxalis is a major French company specialized in the design and manufacturing of innovative custom image sensor solution, with a strong focus on performances and industrial quality over a wide portfolio of markets and applications.

To support its growth, Pyxalis is looking for a design leader engineer.

**MISSIONS AND RESPONSIBILITIES**

As Design leader engineer, you will be responsible for achieving the required specifications and performances, with a focus as well on cost, delays and quality constraints. You will ensure technical teams coordination during the development of image sensors and you will work closely with the project managers

Your missions will encompass the definition and specification phase, architecture up to silicon performance validation of complete circuits. You will be in regular contact with end users and customers as support during the entire project phase, up to production start.

Your missions will include the following:

1. Ensure the technical evaluation and feasibility of new projects, participate in the writing of now technical and financial proposal answer new requests from customers.
2. Establish the statement of work and circuit specification in accordance with customer requirements, propose innovative and unique technical solutions, and tile them into elementary blocs to be designed, as per Top-down approach
3. Execute designs (architecture, modeling, simulation, validation, testability, reliability, robustness) of circuits within respects of specifications (performance, delay, cost) in collaboration with project team members.
4. Manage circuit design documentation as per design process description.
5. Offer technical support to clients with the application engineers.
6. Ensure technical follow-up of projects, lead DFMEA and manage design reviews as per design process.
7. Ensure reporting to project manager on the team design progress (risk management, early warnings, etc.)
8. Contribute to company level know-how (conferences, customer meetings, lessons learned from projects, IP generation, training)
9. Propose innovative solutions that generate patents.

**REQUIRED PROFESSIONNAL EXPERIENCE**

Min 8 years of experience in the field of mixed IC design and associated constraints (foundry management, assembly, test, qualification), ideally in imaging.

**REQUIRED QUALIFICATIONS**



Engineer or MS degree in engineering with a specialty in Electronics, ideally analog design.